DEPARTMENT OF ELECTRONICS ENGINEERING

Application form for the post of Project Associate – I/Junior Research Fellow in research project

- 1. Name in full
- 2. Father's / Husband's name
- 3. Date of Birth
- 4. Nationality
- 5. Gender
- 6. Category
- 7. Mobile number
- 8. Email id
- 9. Address for correspondence:



10. Permanent address:

11. Detailed educational qualification in chronological order starting from 10th standard :

Degree	Specialization	Name of University/Institute	% of Marks or CGPA obtained	Class/Division	Year of passing

12. B.Tech/M. Tech. project title:

13. GATE/NET-JRF qualified: YES/No

Details of examinations like GATE/NET-JRF qualified:-

(Original certificates and Self-attested photo-state copies should be produced at the time of interview).

Name of Examination	Year of Passing	Validity Period	Number of candidates appeared	Marks obtained	AIR Rank	Score / Percentile

14. Research experience (if any) (max 200 words)

15. Publications (if any)

16. Any other relevant information (max. 100 words)

Declaration

I certify that the above information is correct and complete to the best of my knowledge and belief and nothing has been concealed /distorted. If at any time I am found to have concealed / distorted any material/information, my engagement shall be liable to be summarily terminated without/notice/compensation.

Place: Date: